

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application Serial No. .... 10/757,276  
Filing Date ..... January 14, 2004  
Inventor ..... Kevin L. Beaman et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2813  
Examiner ..... Stephen W. Smoot  
Attorney's Docket No. .... MI22-2475  
Customer No. .... 021567  
Title: A Method of Forming a Structure Over a Semiconductor Substrate (as amended)

**RESPONSE TO FEBRUARY 20, 2007 FINAL OFFICE ACTION**  
**ACCOMPANYING REQUEST FOR CONTINUED EXAMINATION (RCE)**

To: Mail Stop: RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

From: D. Brent Kenady  
Tel. 509-624-4276; Fax 509-838-3424  
Wells St. John P.S.  
601 W. First Avenue, Suite 1300  
Spokane, WA 99201-3817

Responsive to the Final Office Action dated February 20, 2007, Applicant amends  
and remarks as follows:

**AMENDMENTS**

Underlines indicate insertions and ~~strikeouts~~ indicate deletions.